## MODEL B980 VME TIME-to-DIGITAL CONVERTER 8 CHANNELS



## **Features**

- Measure time-of-trigger to your reference (time relative mode)
- Measure time-of-event accurately (time stamp mode)
- ▼ Resolution < 50 pS/LSB
- Range 48 bits: 13,743 S unipolar
- ▼ Jitter < 70 pS

The B980 is an 8 channel time measurement module packaged as a single width, 6U VME module. This precise TDC offers two modes of operation. In Time Relative mode, time-of-trigger of eight input channels are measured relative to the reference channel's input. In Time Stamp mode, the time-of-event is recorded on all 9 inputs.

BNC's Model B980 offers several options for retrieving the data out of the module. An optional Microengine is available for high data rate applications. This Microengine offers functions like qualify or discard events, reset channel hits, and store and buffer 'good event' data.

The Model B980 also provides a common inhibit gate. A double hit status register indicates whether a channel receives more than one input during a measurement cycle, status registers indicate this.

Inputs	Nine channels, 8 standard inputs, 1 reference channel, 1 Gate input, 1 Clock TTLinput/output, 2 special-purpose TTL-level AU inputs/outputs	
Lovals	Timing channels - switch selectable as a group for TTL, ECL, NIM or user-set thresholds	
Levels.		
Polarity	Switch selectable for each channel	
Termination	Selectable 50 obms to ground or Hi Z for each channel	
Packaging	Single width 6U Eurocard 16-bit VME module	
Resolution	A8 828125 nS/ISB equivalent to 20 A8 CHz clock	
Dango	48 bits: $\pm 6.871$ seconds bipolar or $\pm 13.743$ seconds unipolar	
littor	$< 70 \text{ nS} \pm \text{timebase}$ iitter RMS Internal clock iitter $< 1 \times 10^{-9}$ measured time	
Integral Linearity	< 10 ps + timebase jitter, twis. Internal clock jitter < 1x10 inteasured time.	
Difforential Linearity	< 1  LSD	
Conformanco	Conforms with ANSI/IFFF 1014-1987 VMFbus spec. Meets VXIC VXI spec rev 1.4 for 'B'-size	
Comormance	VXI module.	
Device Type	16-bit VME register-based slave: A16:D16:D08(EO)	
Timebase	Internal ovenized crystal oscillator. TC below 2x10 <sup>-9</sup> /°C, aging below 1 PPM/year, lockable to	
	external 10 MHz TTL reference or to another B980 or B950 module. Clock I/O is TTL compatible.	
AUX1 AUX2	TTL/HCMOS compatible. These are spare I/O functions that can be programmed to provide or	
	receive application specific information. Contact factory for your r	requirements.
<b>Connector Options</b>	Lemo, SMAor SMB	-
_	Time Relative Mode Diagram	
Gate		
		Channel 1
-	Positive time	•
Events		•
		Channel 8
Reference	İ	
Clear	The reference, T <sub>0</sub> pulse, may occur before, during, or after the events	
	Clears all registers	
		0
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