

16 BIT NIM OUTPUT REGISTER

USER'S MANUAL

Specifications

F(0). A(0) Read back the output register onto R1 - R16 Q response

F(6). A(0-15) Read 8-bit identification

codes Nos. 0-15

Q response

F(7). A(Ø-15) Read 8-bit identification codes Nos. 16-31
Q response

(*)
F(16).A(Ø) Loads the output register
from W1-W16 Q response
Q response

F(18).A(Ø) Selective Set of output register bits from Wl-Wl6 Q response

F(21).A(Ø) Selective Reset of output register bits from Wl-Wl6 Q response

F(24).A(Ø) Disable pulse mode. The content of the output register is connected directly to the outputs

F(25).A(\$\mathref{\pi}\$) When in pulse mode, executes a pulse on the active outputs of duration equal to Sl. No action when in normal mode.

F(26).A(0) Enable Pulse mode. The contents of the output register are gated by F(25).A(0) or Ext. Pulse input before being connected to the outputs

Clear the output register (set all bits zero)

Clear LAM, output register, disable pulse mode

C and I not used

All functions generate X-response

Ordering information

Outputs : Lemo 00 - NIM level

Ext. Pulse : Lemo 00 NIM level
Input nigh inpedance
bridged input

Pulse shape: tr max 6ns.

tf max 8ns.

Pulse shape:

minimum FWHM width: 12 ns.

minimum FWHM width

at output: 8 ns.

propagation delay: 19ns. + 2ns.

LEDs display contents of the output register and the module status (normal or pulse mode)

(*)
If required, the OR 1320 generates a LAM when programmed in pulse mode with the external pulse strobe, after the end of the strobe signal.

This facility is selected by a jumper located between IC14 and IC18. The associated CAMAC commands are:

 $F(8).A(\emptyset)$ Test LAM

Q response if L=1

F(10).A(0) Clear LAM

Power requirements :

+ 6V : 1.4A - 6V : 0.6A